FIG. 1.1

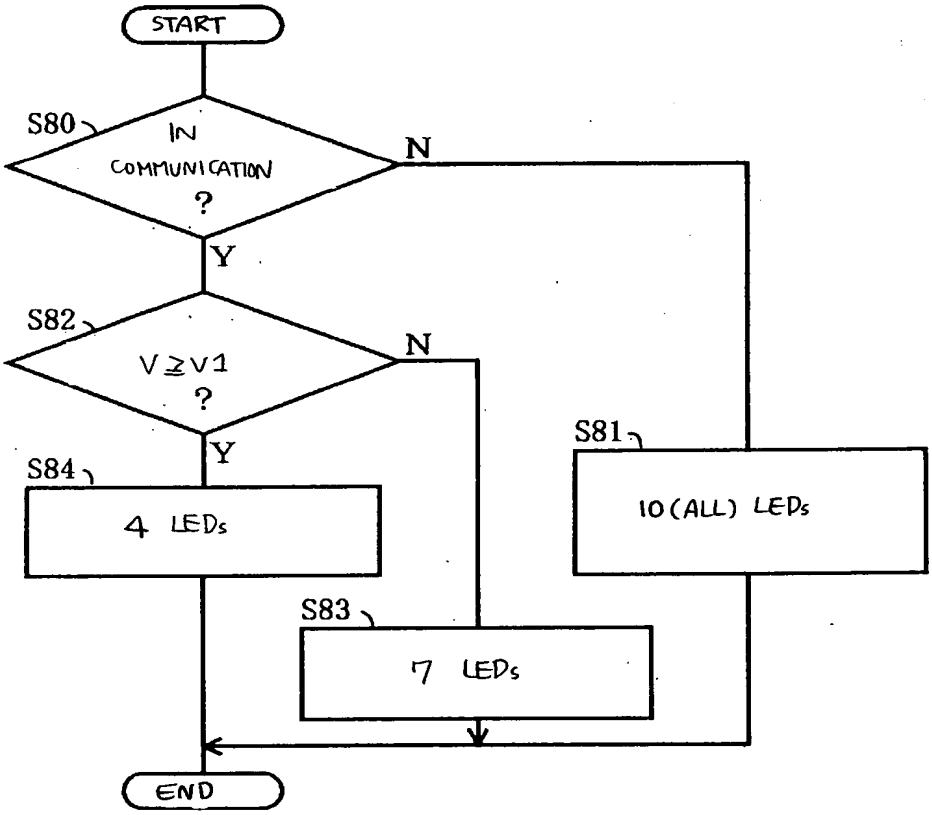


FIG. 1
PRIOR ART

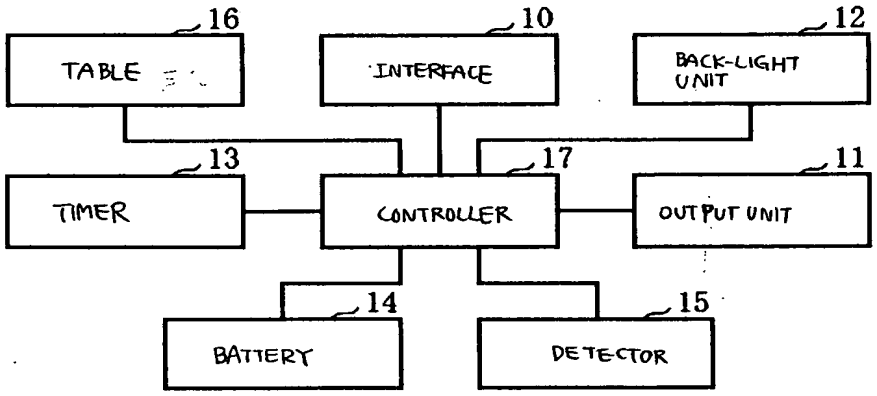


FIG. 2
PRIOR ART

RESIDUAL POWER V	NUMBERS
$V \geqq V_1$	M_1
$V_1 > V \geqq V_2$	M_2
$V_2 > V \geqq V_3$	M_3
⋮	⋮

FIG. 3
PRIOR ART

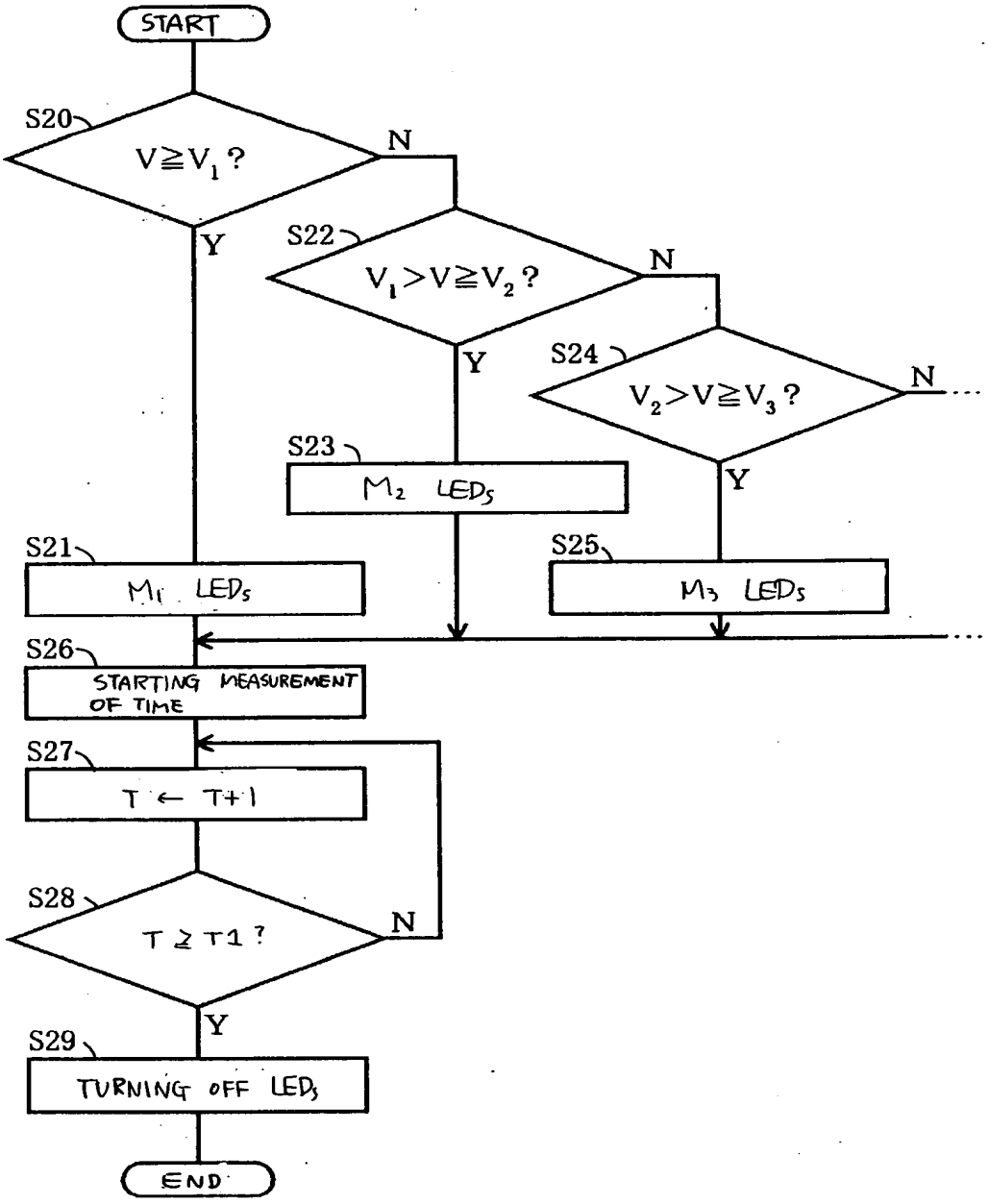


FIG. 4

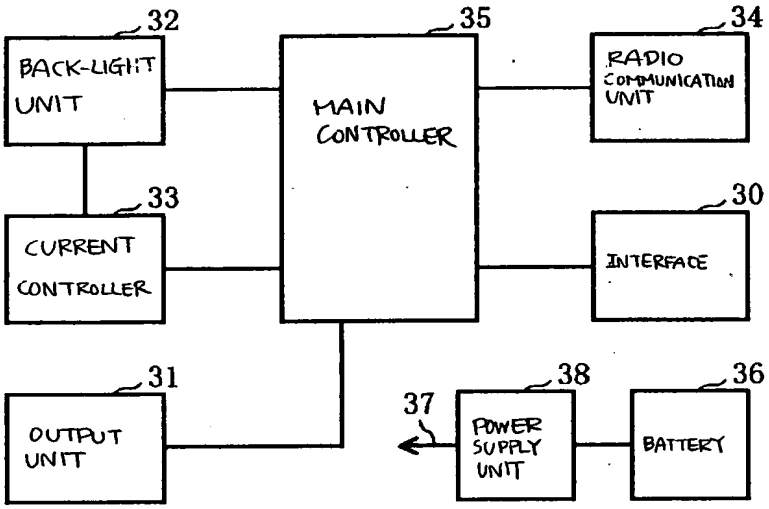

32
BACK-LIGHT UNIT
35
MAIN CONTROLLER
34
RADIO COMMUNICATION UNIT
33
CURRENT CONTROLLER
30
INTERFACE
31
OUTPUT UNIT
37
38
POWER SUPPLY UNIT
36
BATTERY

FIG. 5

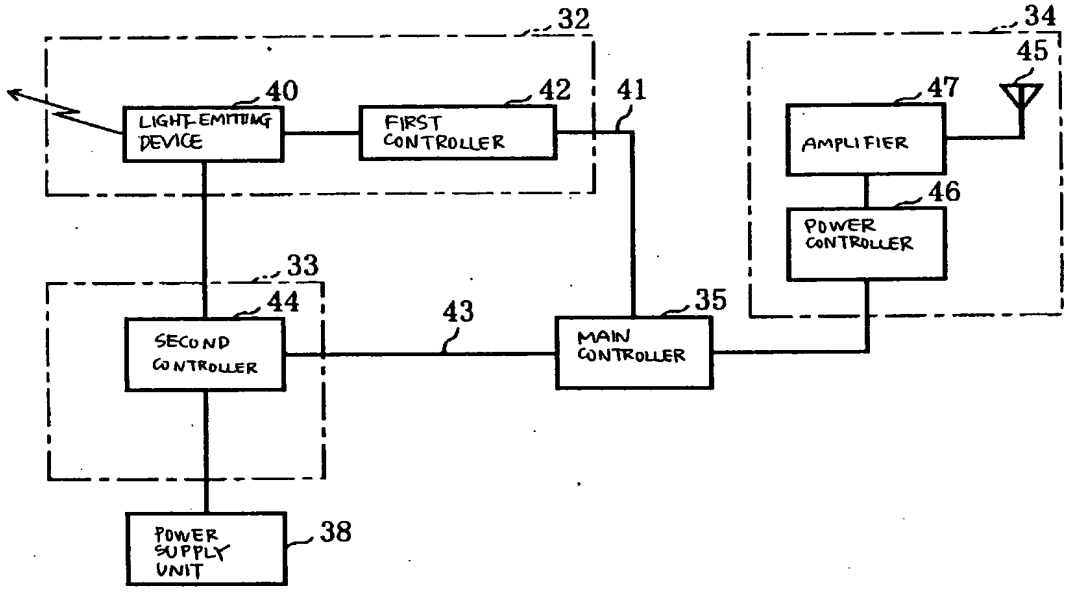

32
34
45
40
LIGHT-EMITTING DEVICE
42
FIRST CONTROLLER
41
47
AMPLIFIER
46
POWER CONTROLLER
33
44
SECOND CONTROLLER
43
35
MAIN CONTROLLER
POWER SUPPLY UNIT
38

FIG. 6

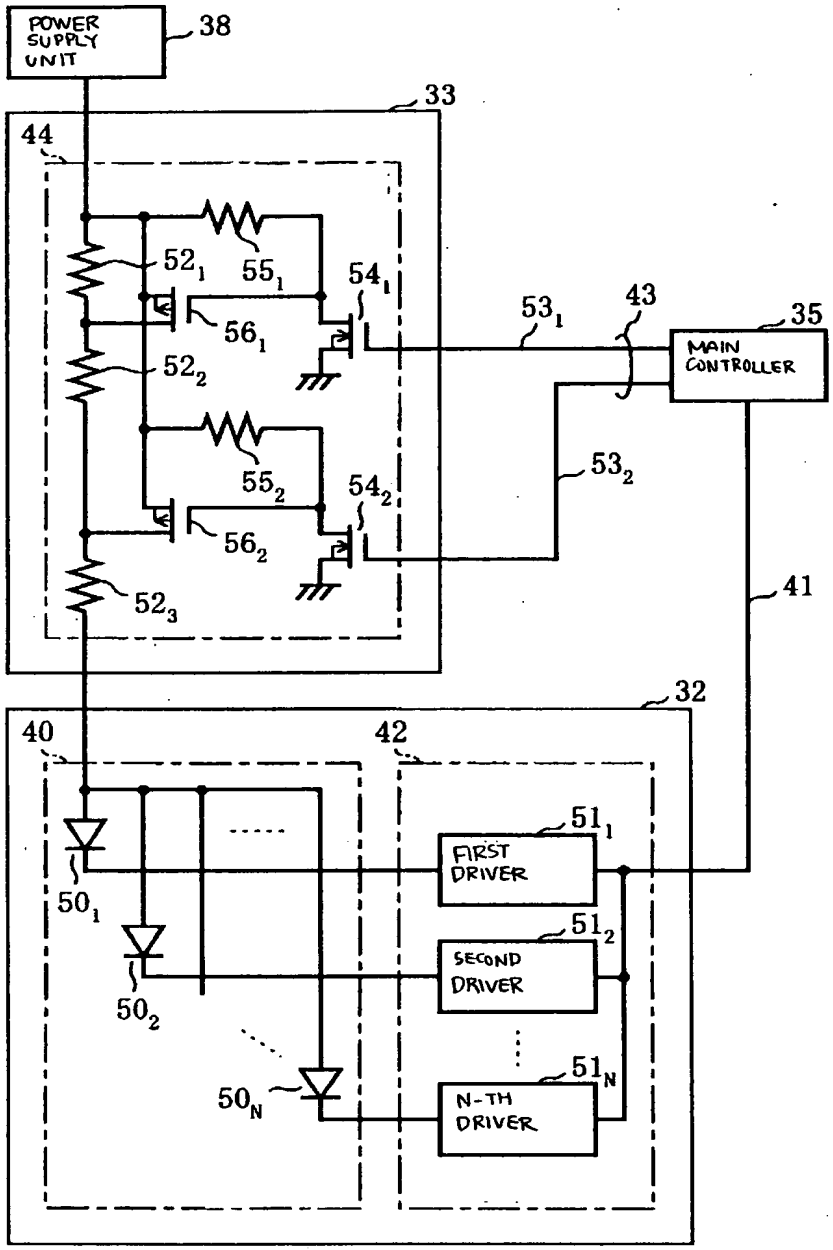

POWER SUPPLY UNIT
38
33
44
52_1
55_1
54_1
56_1
53_1
43
35
MAIN CONTROLLER
52_2
55_2
54_2
56_2
53_2
52_3
41
32
40
42
51_1
FIRST DRIVER
50_1
51_2
SECOND DRIVER
50_2
51_N
N-TH DRIVER
50_N

FIG.7

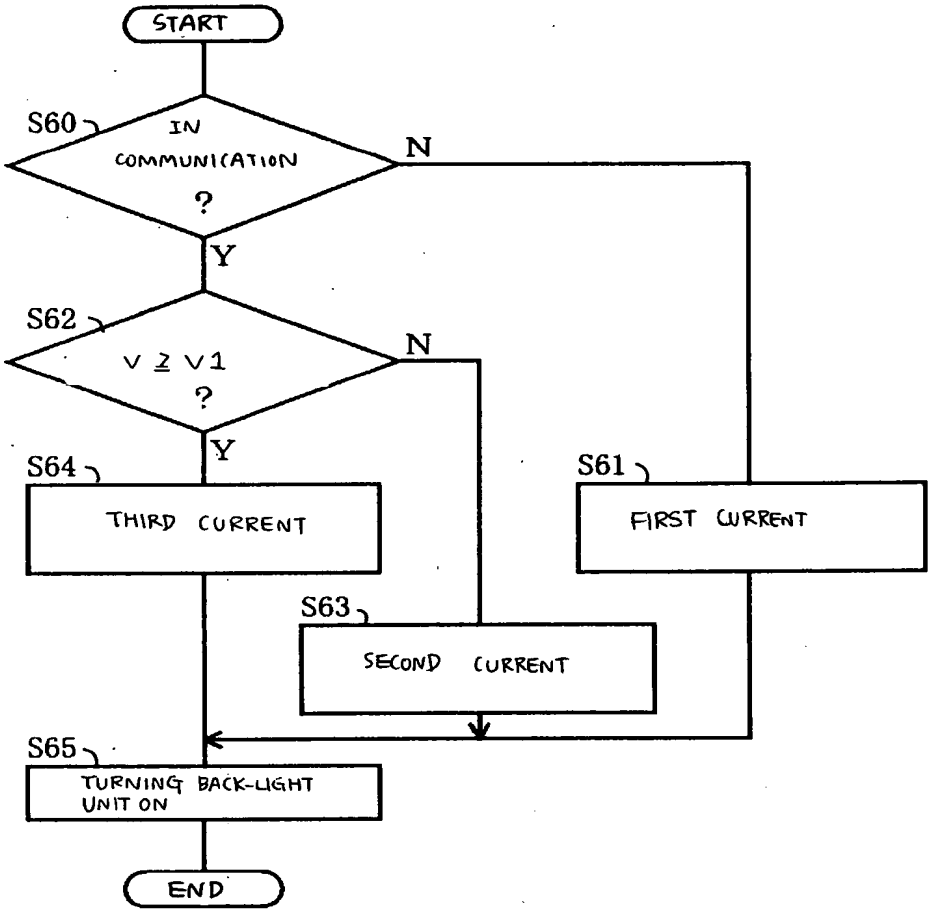


FIG.8

CONDITIONS		FIRST SIGNAL 53_1	SECOND SIGNAL 53_2
RADIO COMMUNICATION ?	GREATER THAN V1 ?		
NO	—	LOW	HIGH
YES	NO	HIGH	LOW
YES	YES	LOW	LOW

FIG. 9

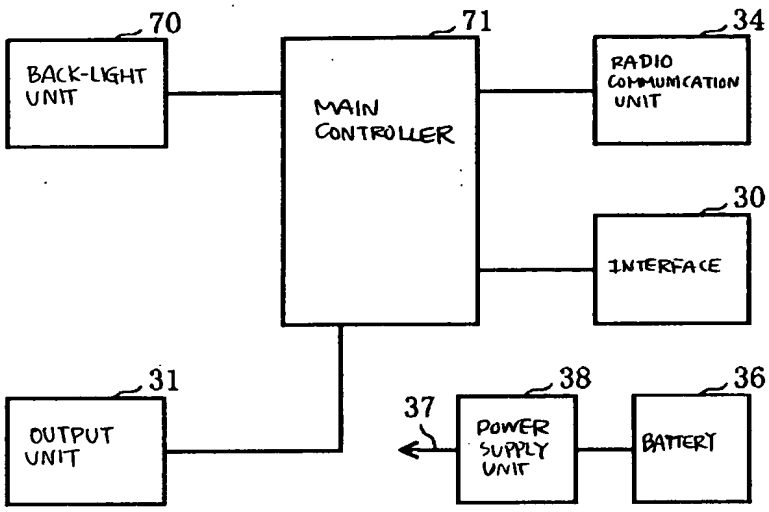
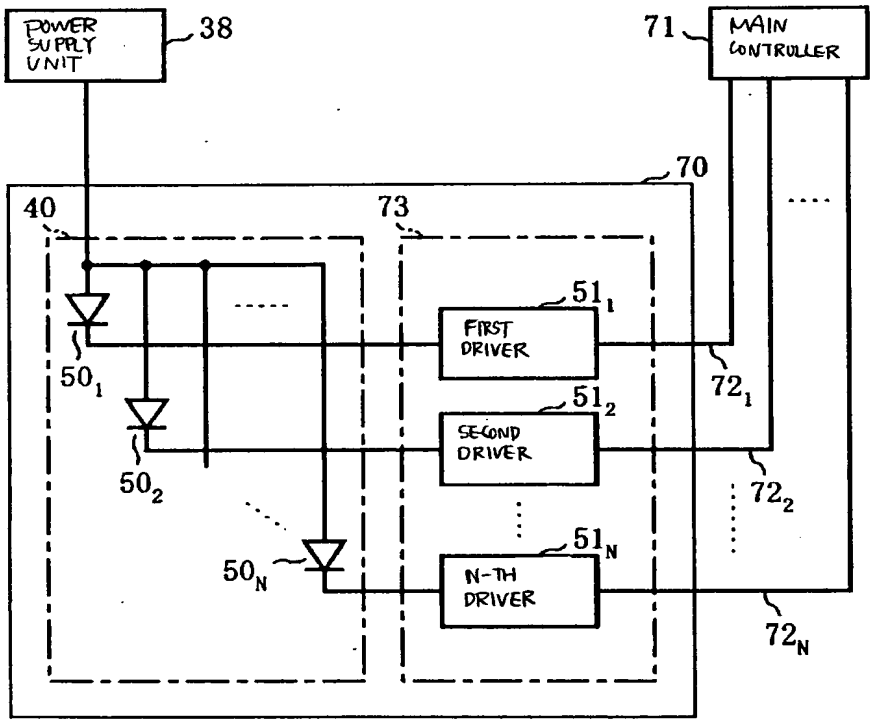

70
BACK-LIGHT UNIT
71
MAIN CONTROLLER
34
RADIO COMMUNICATION UNIT
30
INTERFACE
31
OUTPUT UNIT
37
38
POWER SUPPLY UNIT
36
BATTERY

FIG. 10


POWER SUPPLY UNIT
38
71
MAIN CONTROLLER
70
40
73
50_1
50_2
50_N
51_1
FIRST DRIVER
51_2
SECOND DRIVER
51_N
N-TH DRIVER
72_1
72_2
72_N